



Department of Electronics & Communication Engineering

National Institute of Technology Durgapur

Durgapur - INDIA

<http://ece.nitdgp.ac.in> ; <http://www.nitdgp.ac.in> ; +343-2547375(Fax)

January 27, 2016

C2SD-SMDP Walk-in-Interview: Urgent

With reference to the Advt. No NITD/ECE/C2SD/2016/Staff/Walk-in-Interview/01 dated January 08, 2016 it is hereby notified to

ALL Candidates applied ONLINE and Submitted their resume as per the supplied format are eligible to attend the Interview / Selection Process, provided they have the **requisite qualification mentioned in the advertisement**

As the number of candidates are large, there will be a written test which will start at 10 AM (Sharp) of Jan. 29, 2016 in the ECE Department. Interview of the shortlisted candidates will follow any time after 10:30AM on the same day.

Please note the revised time line:

<u>Event:</u>	<u>Date Jan. 29, 2016</u>
Reporting of candidates	9:50 AM or earlier
Candidates must occupy their Seat for Written Test	9:59 AM or earlier
Written Test begins	10:00AM
Written Test Ends	10:30AM
Display of Shortlisted Candidates	10:45AM tentative
Interview of the Shortlisted Candidates begin	10:46AM tentative

Candidates MUST attend the written test with a Valid ID proof. The timing and dates mentioned here are tentative. No TA /DA is admissible for attending the interview. Time line mentioned in the advt. stands modified/withdrawn.

(Ashis Kumar Mal & Rajat Mahapatra)

Investigators, C2SD-SMDP

Phone: + 88055/ +88126



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NITD/ECE/C2SD/2016/Staff/WalkIn/01

January 08, 2016

WALK-IN-INTERVIEW

Recruitment of the Project Staff through Walk-in-Interview (Temporary Faculty/Lab Engineer,/Project Assistant) work under the following DeitY-Sponsored Project of 5 years duration.

“Chips to System Design- Special Manpower Development Project (C2SD-SMDP)”

Interested candidates may attend the interview with original and attested copies of all academic and experience certificates (if any) and testimonials **on Jan 29, 2016 at 09.30 AM in the Department of ECE, NIT Durgapur.**

Application form is available here: <http://goo.gl/forms/8LGgXcVVHx>

Details about the position(s):

Sl	Post	Qualification	Duration	Consolidated Salary (PM)
1	Guest/Temporary Lecturer	ME/MTech/PhD in the field of Microelectronics/VLSI with experience in CAD tools and Chip design/Linux	Initially for the period of 1 year which may be extended	Rs 40,000/= (Per month)
2	Lab. Engineer	M. Tech/M.E. in Microelectronics/VLSI with experience in CAD tools	As above	Rs. 34,000/= (Per month)
3	Project Assistant	B. Tech/ M. Tech in Electronics & Comm. Engg. with experience in CAD tools	As above	Rs. 20,000/= (Per month)

Terms and conditions:

1. The above position is purely contractual, initially for 1 year and renewable after every year.
2. No TA/DA will be paid for attending the interview.
3. Candidates have to appear for walk-in-interview with bio-data, passport photos, original certificates and attested photocopies of all certificates, mark sheets, degrees and testimonials. **They are also required to apply online through the link: <http://goo.gl/forms/8LGgXcVVHx>**
4. Reporting time at the Department of ECE, NIT Durgapur for the Interview: Jan 29, 2016 at 9:30 AM
5. Candidates reaching late will not be entertained.
6. Candidates may be allowed to enrol for Ph D program subject to NIT Durgapur regulations.
7. **Last date of filling online application is Jan 27, 2016.**

Ashis Kumar Mal & Rajat Mahapatra
Chief & Co-Chief Investigators, C2SD-SMDP
Email: c2sdgp@gmail.com, phone: 0343-275 5202/4377



National Institute of Technology Durgapur
Durgapur-713209, INDIA
Application Form for C2SD-SMDP Project Position
(All Posts are Temporary)

Paste here a
recent
Passport size
Photograph

1. Post Applied for : Guest Lecturer / Lab Engineer / Project Assistant _____
2. Name of the Candidate (BLOCKLETTER): _____
3. Father's Name(BLOCKLETTER): _____
4. Mother's Name(BLOCKLETTER): _____
5. (a) Date of Birth:(DD/MM/YYYY) _____ (b)Sex(Male/Female/Other): _____
(c) Marital Status: Married/ Single (d)Category: SC/ST/OBC/PWD/Open _____
6. Previous Research experience: _____
7. Publication(s), if any: _____
8. GATE/ NET (if any):Qualified (Yes/No):Score: _____ Rank: _____
Specialization: _____ Year: _____
9. Academic Qualification: (Starting from Standard 10 or equivalent Examination)

Name of Exam. Passed	Name of the School/College/ Institute/ University	Year of Passing	Discipline/ Specialization	Percentage of Marks/ CGPA

10. (a) Address for Communication: _____
(BLOCKLETTER) _____
- (b) Contact No (Mob) _____
- (c) E-mail ID _____
11. Name of the Referee with email _____
12. Experience on Cadence/ ___ Synopsys/ ___ Mentor/ ___ Linux / ___ Others/ ___ _____ (in years)

I do here by declare that the information furnished in this application is true to the best of my knowledge and belief. If selected, I promise to abide by the rules and regulations of the Institute.

Date:
Place:

Full Signature of the Applicant

ELIGIBILITY

1.M.Tech./M.E./ equivalent in Microelectronics & VLSI / or ECE with strong exposure to VLSI Design, CAD Tools with at least 6.5 CGPA or 60 percent marks in aggregate from a recognized technical institute or university/ university as a Full time program

2.B. Tech/B.E./ in ECE with at least 7.5 CGPA or 70 percent marks in aggregate from a recognized technical institute or university/as a Full time program.

3.You can download the editable application form (doc) from [Download Form](https://drive.google.com/open?id=0B8rkNZZfKj2KcG9VamNFY1JFUTQ)
<https://drive.google.com/open?id=0B8rkNZZfKj2KcG9VamNFY1JFUTQ>

IMPORTANTDATES

LASTDATEOFRECEIPTOFAPPLICATIONS-	27/01/2016
DATEOFPUBLICATIONOFSHORTLISTONTHEWEBSITE	- 28/01/2016
DATE OF INTERVIEW	From 10AM 29/01/2016
NOTIFICATION TO SELECTED CANDIDATES	30/01/2016
ACCEPTANCE OF OFFERS BY	31/01/2016

APPLICATION CHECKLIST(AT THE TIME OF INTERVIEW)

The completed application (duly signed) must be produced with following self-attested documents at the time of interview. Candidates, however are advised to mail an advance copy to **c2sdgp@gmail.com**

- i) Photocopy of marks sheet/Grade card of the secondary(10), higher secondary (10+2),
- ii)Photocopy of the certificate/provisional certificate of B.E/B.Tech, M.E/M. Tech, PhD examination
- ii) Photo copy of proof of date of birth
- iv)No-objection certificate from the present employer, if applicable.