

<u>Proposal for Short term training cum internship program on VLSI, Embedded Systems & IoT</u> (VLESI -2022) : for students

The Department of Electronics & Communication Engineering is organizing a four week self financed summer training program on "VLSI Design, Embedded Systems and IoT (VLESI - 2022)" during 09th July to 08th Aug. 2022.

The course is designed for (2nd Year/ 3rd Year) **Undergraduate/ (1st year) Post-Graduate students** of Electronics & Communication Engineering, Engineering and Electrical Engineering, Computer Sc. & Engineering and other associated branches in the area of VLSI design, Embedded Systems and IoT. The course covers theoretical as well as practical aspects in the respective fields. The VLSI Laboratory of the department is equiepped with **state of the art EDA tools like, Cadence, Mentor Graphics, and Xilinx** etc. ECE Laboratories are also equipped with basic and advanced Microcontrollers, Microprocessors like 8051, AVR, 8085, 8086, PIC and ARM with their compiler and software. We also intend to invite industrial professionals and faculty from reputed institutes for expert lectures (offline/online) in the field of embedded systems/IoT and VLSI Design.

Accommodation for outstation students may be arranged in the institute hostel (subject to availability). However, nominal fee for hostel accommodation as per the institute norms will be charged from the students.

(1) Eligibility: B.Tech/BE (Electronics / Electronics & Communication/ Electronics & Telecommunication/ Electrical & Electronics/ Electrical Engineering/ Computer Sc. & Engineering or similar discipline) 2nd/3rd/4th and PG 1st year Students of any UGC/ AICTE recognized University/ Institute/ College.

(2) Scope: The Course is divided into two streams (i) VLSI Design-will cover Device & Circuit Concepts in VLSI and related Software and Hardware (ii) Embedded Systems & IoT- will cover Embedded systems and their applications through hardware and software. (Topic wise details will be given separately)

(3) Fees & mode of payment: Course Fees is Rs. 7,500/- (Rs. Seven Thousand Five hundred only) per candidate. However, course fees for NIT Durgapur Students is Rs. 5,000/- (Rs. Five Thousands only). The course fee should be paid through NEFT as per the following details:

Account Name: VLESI-22 Account No: TBD IFSC Code: SBIN0002108 Bank Name & Branch: SBI, NIT Durgapur The course fee is non-refundable. shortlisted for the course. The selection will be based on first cum first serve basis. **Only first 20 candidates will be accommodated** and, course fee will be refunded in case student is not selected.

(4) **Duration:** This is a one month training Program. The tentative start date of course is 9th July 2022

(5) **Application Procedure:** Application form and information brochure will be available from 24th June 2022 to 7th July 2022 on the website https://www.nitdgp.ac.in. The applicants are required to download the application form and send the duly filled form to the *The Coordinators, VLESI-2022, Department of Electronics and Communication Engineering, National Institute of Technology Durgapur, Durgapur-713209, along with the Receipt of the Application Fee paid on or before 7th July 2022.*

REGISTRATION FEE:

Type of Registration	Fee(INR)
Students Outside NIT Durgapur:	Rs.7,500/-
Student from NIT Durgapur:	Rs. 5000/-

IMPORTANT DATES:

Issue of Application form	24 th June, 2022
Last date of receipt Application form	7 th July, 2022
Confirmation will be intimated by:	7 th July, 2022 (through e-mail)
Course duration:	09 th July to 08 th Aug. 2022

CONTACT ADDRESS:

Prof. Ashis Kumar Mal, Dr. Hemanta Kumar Mondal & Dr. Nilanjan Chattaraj Program Coordinators, VLESI-2022, Department of Electronics and Communication Engineering, National Institute of Technology Durgapur, Durgapur-713209 Phone No.: 91-343-2754377(O)/9434789047/9434789050 akmal@ece.nitdgp.ac.in

Note:

- Candidates should mention their e-mail and contact number in application form with CV.
- Program fee does not include food/refreshments. Lunch can be arranged on payment basis
- Candidates must bring their own laptops.

Coordinators (VLESI-2022) ECE, NITD