NationalInstituteofTechnology, Durgapur Department of Electrical Engineering Mahatma Gandhi Avenue, Durgapur-713209, West Bengal

Advt. No: NITD/EE/JRF/IEMUGV/04/2023 Date: 01.06.2023

Applications in specified format attached are invited from Indian citizens for the following post to work in a project funded by DRDO, Govt. of India.

Sl. Nameand Numberofthe **Qualification required** Name of the project no. postandsalary/fellowship 1 Junior Research M.E/M.Tech **Improved Energy** Fellow: in Electrical Fellowship will be @ Rs. Engineering (EE)/ Electrical and Management and 31,000/- [*HRA @16%p.m. Electronics Engineering (EEE) with Control of a Hybrid as per DRDO guideline] minimum CGPA 6.5 or 65% marks **UGV** for Combat Use Post number: 01 recognized anv Institute/University. (*HRA will be provided OR additionally only if hostel B.E/B. Tech in EE/EEE with valid

score

minimum 7.5 CGPA or 70% marks

from any recognized Institute/

Other Requirement: Knowledge of

and

NET/GATE

University

How to apply:

facility is not available on

campus, following selection);

Fellowship is available upto

End of Project (12/11/2024).

Interested candidates are advised to send the application (format attached) giving full particulars of Educational Qualification along with attested copies of the mark sheets and certificates, Experience, Publication, Research Interest etc. to any one of the undersigned with advt. no. as the subject line within **June 23**, **2023**. For the further details pl. contact (Mobile No: 9434788173/ 9434788171). Shortlisted candidates, date of interview other essential information will be notified on the website on **June 28**, **2023**.

MATLAB is preferred.

Terms and Conditions:

- 1. Selected candidates are expected to play a major role in the fundamental research, simulation and implementation activities related to the project in collaboration with the other involved Institutes/Universities.
- 2. Financial assistance for attending Team meetings, Focus area meetings, National conferences etc. will be funded from the project whenever scheduled.
- 3. The applications will be screened and short-listed candidates will be called for interview (Online).
- 4. The above position is purely temporary and is **upto End of Project** (12/11/2024), **but subject to condition of the candidate performance** (Performance will be evaluated for every six months). Selected candidates will not be entitled to claim for regular appointments/absorption in this institute.
- $5.\ Candidates\ called\ for\ interview\ should\ send\ the\ scan\ copy\ of\ all\ the\ original\ mark-sheets,\ certificates\ etc.$
- 6. Canvassing in any form will disqualify the candidature.

Dr. Jayati Dey

Jayati Dey

Principal Investigator& Associate Professor

Department of Electrical Engineering

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Co-Investigator& Professor

Department of Electrical Engineering

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Mobile: 9434788171

FORMAT FOR APPLICATION

Paste a

Name (in Capital letters):				recent passport size
Date of Birth:				photograph
Address (including mobile & Email ID):				here
Qualification:				
Examination	Discipline	Board/University	Percentage(%)	Year
M. Tech/M. E				
B. Tech				
ClassXII				
ClassX				
	<u>l</u>			
Any National level Examination (NET/GATE or any others):				
Awards (if any):				
Research experience (if any):				
Publication (if any):				

Full Signature of the Candidate